Frequency Technology

SX3CTQ

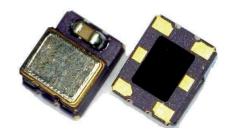
HCMOS SURFACE MOUNT TEMPERATURE COMPENSATED CRYSTAL CLOCK OSCILLATOR

Frequency Technology

FEATURES

- Miniature package
- Low jitter

 $3.2 \times 2.5 \times 1.7 \text{ mm}$



Item	Specification							
Frequency Range	10 MHz ~ 245.0 MHz							
Output Signal	CMOS							
Supply Voltage Vdd	+2.5V ±5% +3.3V ±5%							
Supply Current Idd	30.0 mA max , Frequency dependent							
Frequency Tolerance	±2.0 ppm at 25°C ±2°C (one hour after reflow)							
Frequency Stability vs Temperature (see options)	-20° to $+70^{\circ}$ C -30° to $+75^{\circ}$ C -30° to $+85^{\circ}$ C -40° to $+85^{\circ}$ C O = available	±0.5 ppm	±1.0 ppm	±1.5 ppm	±2.0 ppm	±2.5 ppm		
Frequency Stability vs Aging	±1.0 ppm max. per year at 25°C							
Frequency Stability vs Voltage Change	±0.2 ppm max., for a ±5% input voltage change							
Frequency Stability vs Load Change	± 0.2 ppm max., for a $\pm 10\%$ load condition change							
Output Level	VOH ≥ 0.9 Vdd			VOL≤0.1 Vdd				
Output Load	15 pF							
Symmetry	45 / 55 %							
Rise / Fall time Fr/Ff	3.0 ns max.							
Tri-state function	pin #2 = high or open pin #2 = low			pin #4 ==> oscillation pin #4 ==> high impedance				
Start-up Time	5 ms max.							
Integrated Phase Jitter (12 kHz to 20 MHz band)	0.8 ps typical							
Packing Unit	1000pcs / reel							
Soldering Condition	260°C , 10 sec x2 max							

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OPTIONS & ORDERING INFORMATION

SX3CTQ						MHz
	Supply Voltage *	Operating Temp. *	Temperature Stability *	Tri-state Function	Package type	Frequency in MHz
	25 = +2.5V	F = -20° / +70°C	$0.5 = \pm 0.5 \text{ ppm}$	E2 = Tri-state , pin 2	6P = 6-pad version	Please specify the
	33 = +3.3V	G = -30° / +75°C	$1.0 = \pm 1.0 \text{ ppm}$			frequency in MHz
		H = -30° / +85°C	$1.5 = \pm 1.5 \text{ ppm}$			
		K = -40° / +85°C	$2.0 = \pm 2.0 \text{ ppm}$			
			$2.5 = \pm 2.5 \text{ ppm}$			

 $[\]ensuremath{^{*}}\xspace$ Note : Not all combinations are possible , please consult us.

OUTLINE DIMENSIONS (MM)

